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| APPLICANTS | | | | | | | · | |
| Akio Sugiyama, Ka | awasaki-shi, JAPAN; | | | | | | | |
| ** CONTINUING DATA *** | HU | | | | | | | |
| ** FOREIGN APPLICATIO JAPAN 2002-2688 | | | | | | | | |
| IF REQUIRED, FOREIGN ** 12/05/2003 | FILING LICENSE GRANTED | | | | | | | |
| Foreign Priority claimed | □yes □ | | STATE OR | SHEETS | | TOTAL | | INDEPENDENT |
| 35 USC 119 (a-d) conditions met ves no Met after Allowance Verified and Acknowledged Examiner's Signature Initials | | | COUNTRY JAPAN | DRA | AWING 15 | CLAIMS 16 | | CLAIMS 7 |
| ADDRESS 23373 SUGHRUE MION, PLLC 2100 PENNSYLVANIA AV SUITE 800 WASHINGTON , DC 20037 | ENUE, N.W. | | | | | | | |
| TITLE | | | | | | | | |
| Data read circuit in a semiconductor device featuring reduced chip area and increased data transfer rate | | | | | | | | |
| FILING FEE FEES No RECEIVED No 1086 | No to charge/credit DEPOSIT ACCOUNT | | | | All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue) Other Credit | | | |